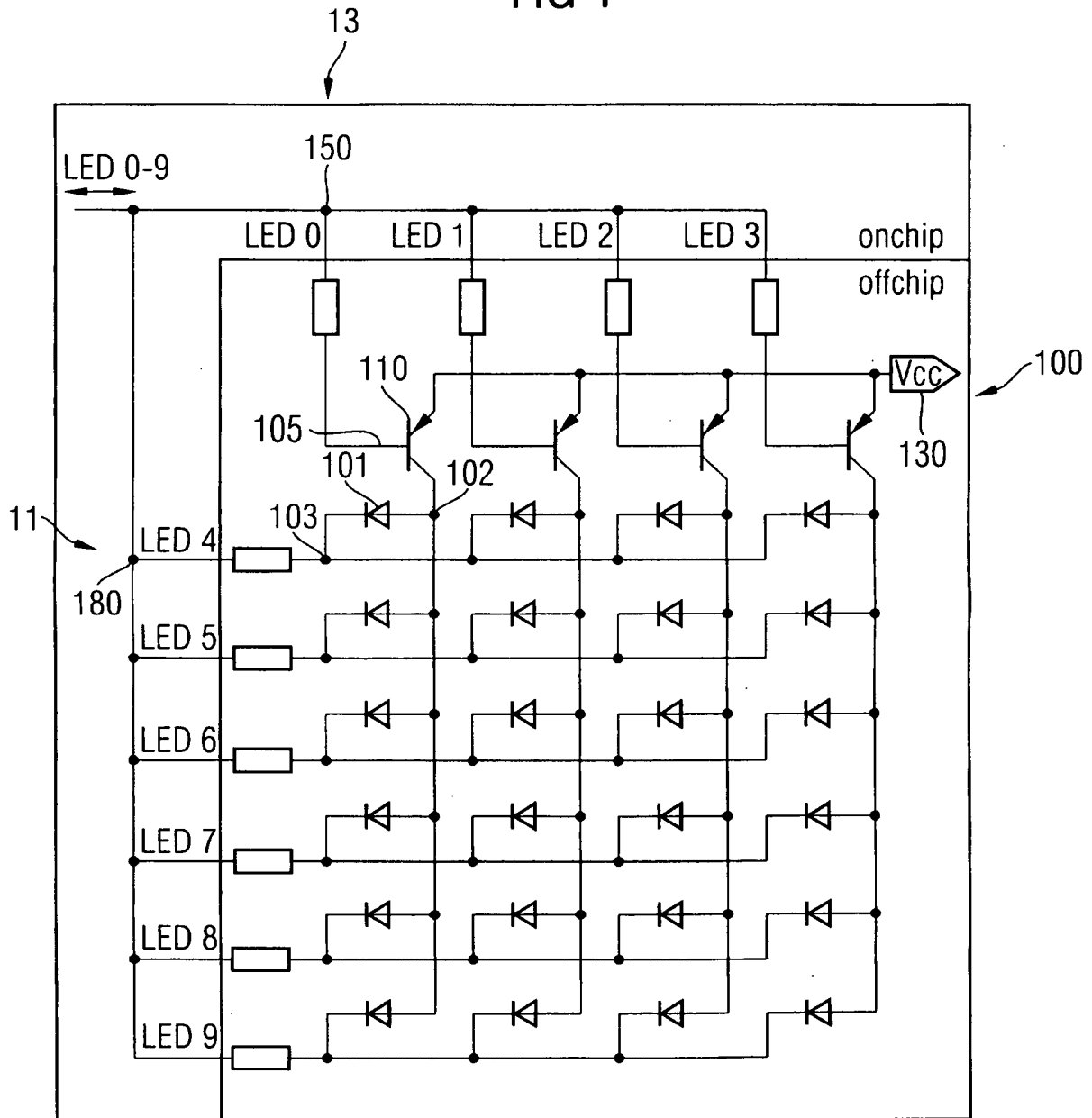


1/6

FIG 1



LED matrix as implemented in INCA-IP

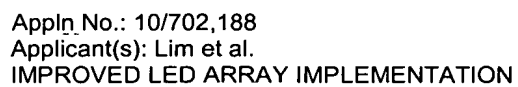
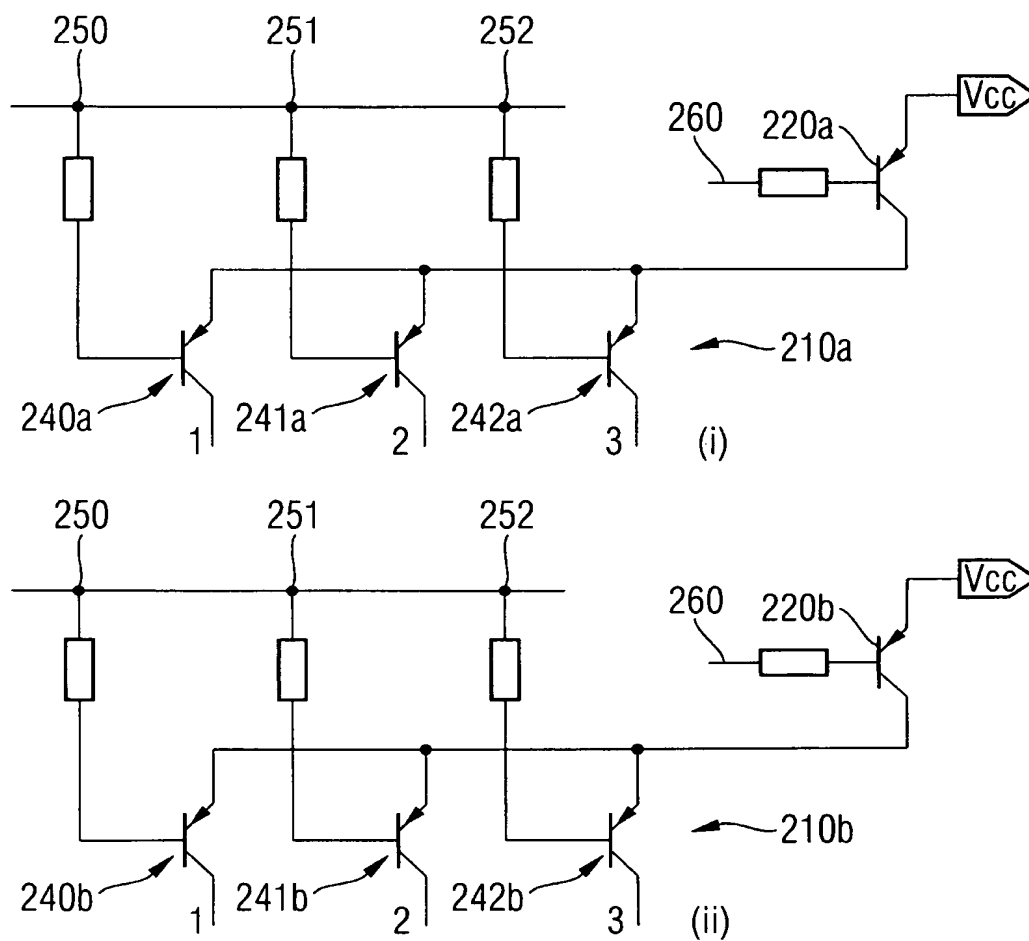


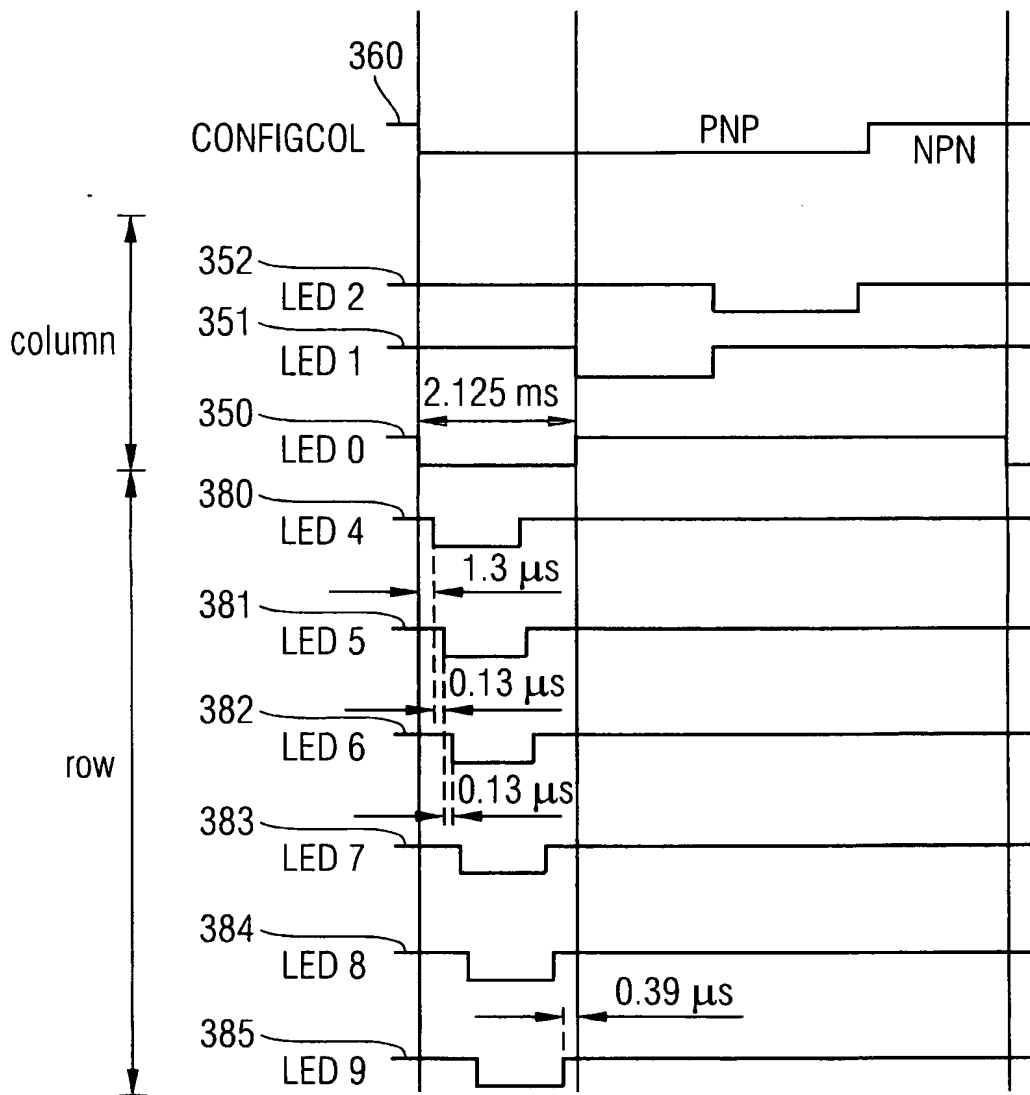
FIG 2



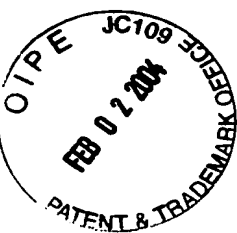
Row Decoding for output lines 1-4

3/6

FIG 3



Timing Diagram of signals on LED output lines



4/6

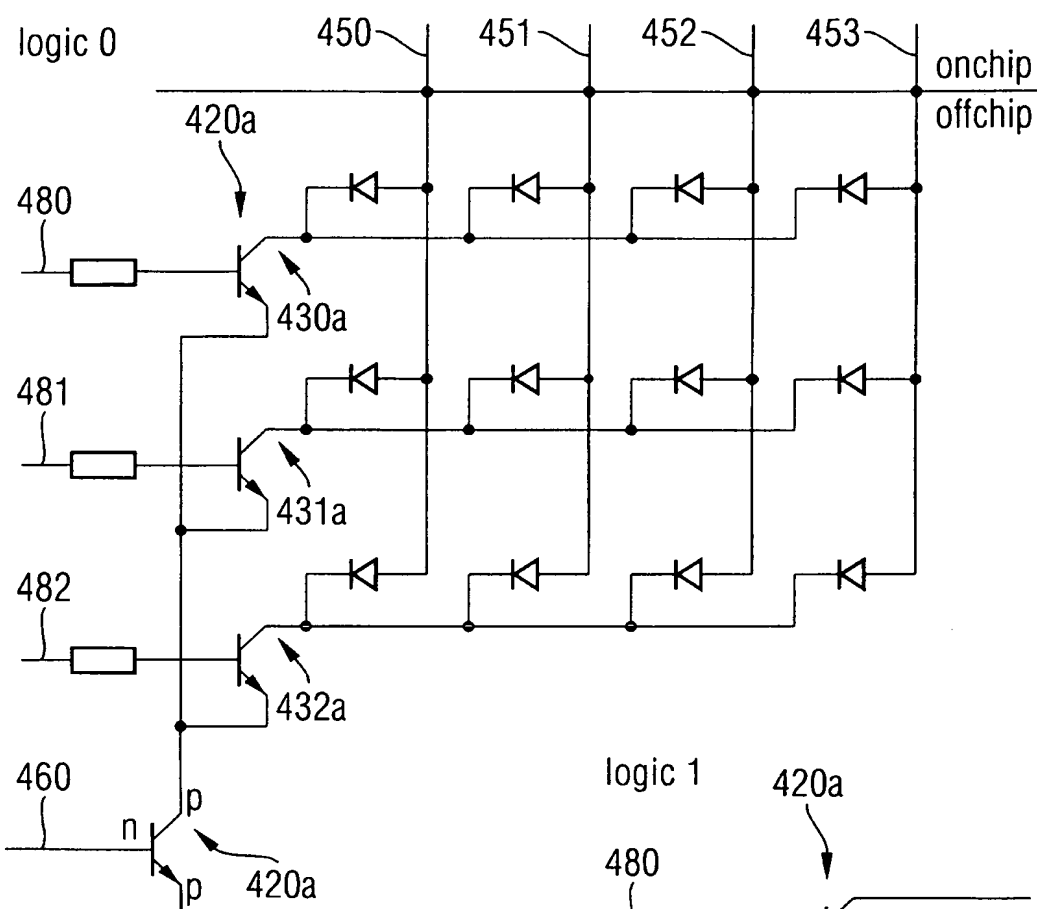


FIG 4(i)

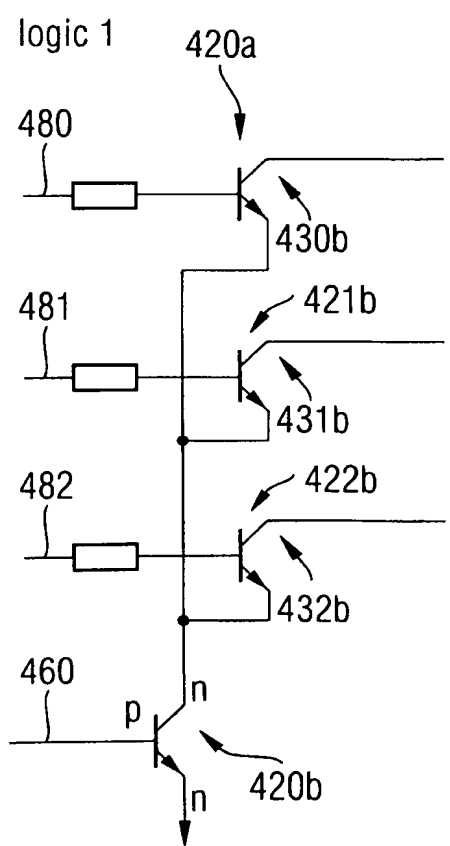
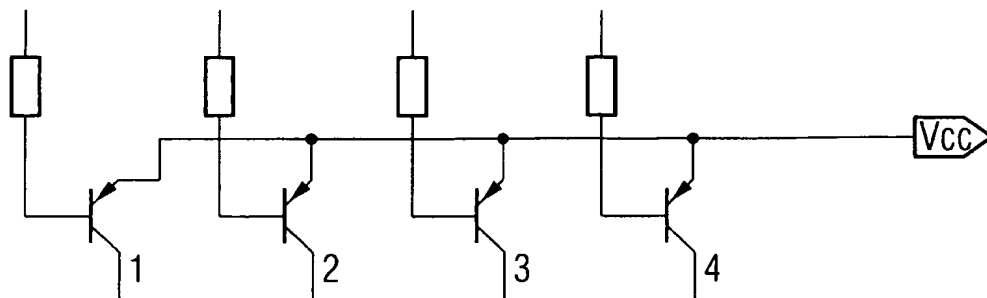


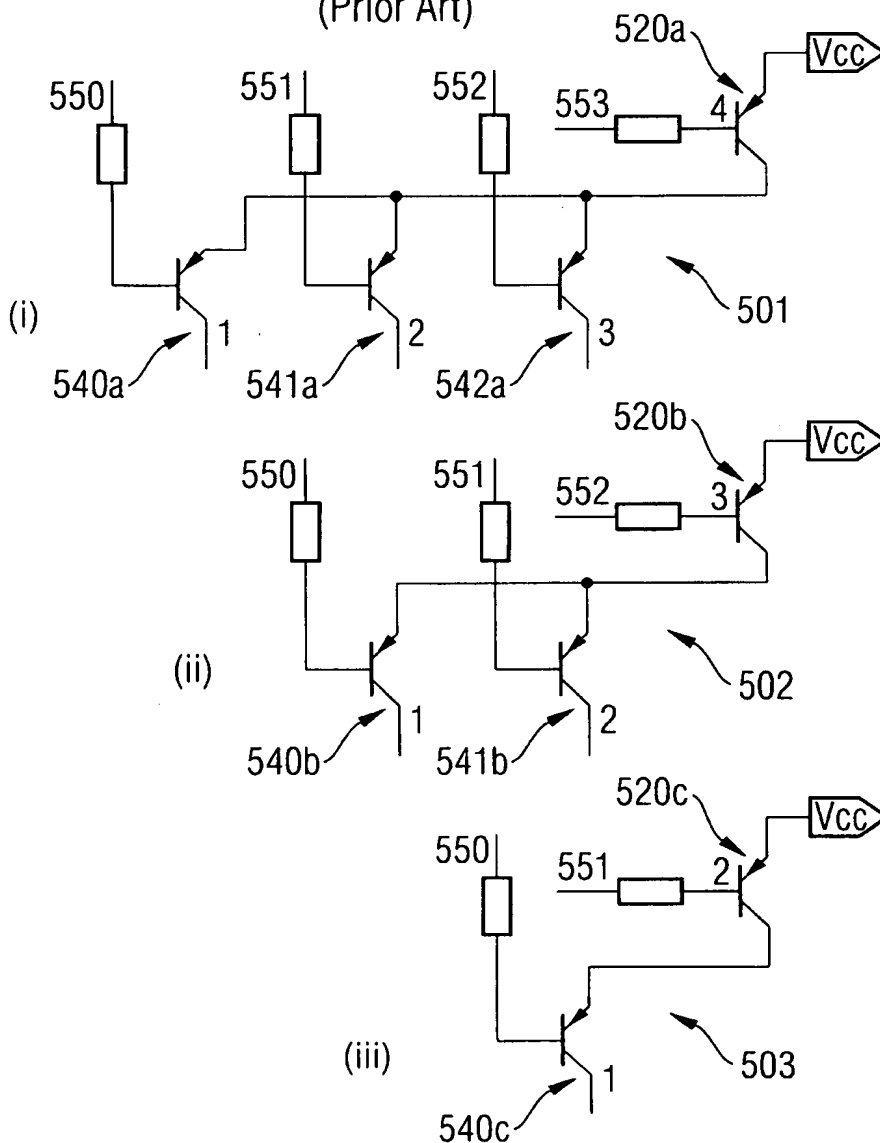
FIG 4(ii)

5/6

Full-Decoding

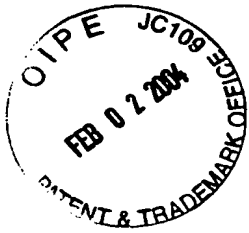


(Prior Art)



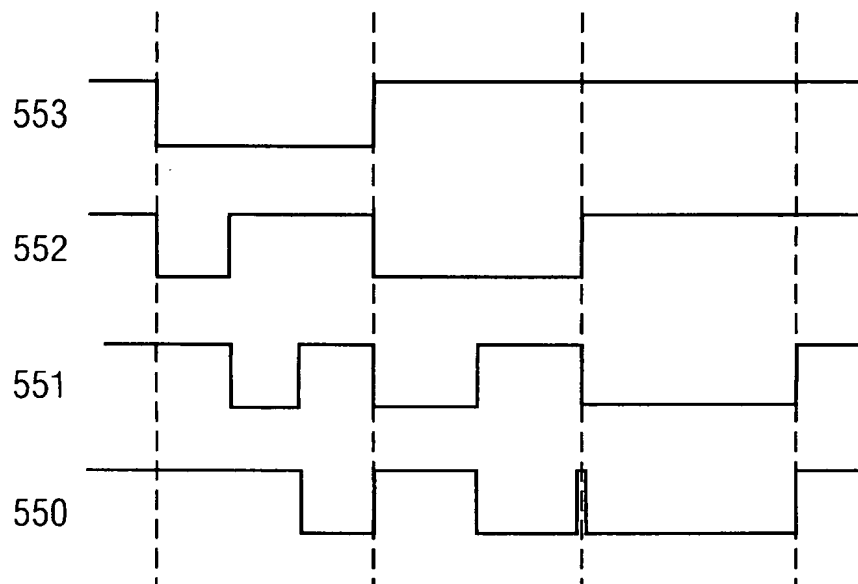
Decoding technique using all the column control input

FIG 5



6/6

FIG 6



Timing Diagram with full-decoding